

CHECKED PARENT
10/613,169

	Type	Ref #	Hits	Search Text
1	BRS	S1	114	("or" adj1 gate) and ((dual adj1 gate\$1) or (double adj1 gate\$1))
2	BRS	S2	7	("or" adj1 gate) with (precharge adj1 transistor)
3	IS&R	S3	1	("20020004271").PN.
4	IS&R	S4	1	("20020140008").PN.
5	BRS	S5	187	("or" adj1 gate) and (precharge adj1 transistor)
6	IS&R	S6	11	((("20030058001") or ("20020180486") or ("20020084803") or ("20020081808") or ("20020047727") or ("20010022521") or ("6,433,609") or ("6,420,905") or ("6,404,237") or ("6,376,317") or ("6,188,243"))).PN.
7	IS&R	S7	5	((("6,104,068") or ("6,097,221") or ("6072,354") or ("4,468,574") or ("4,300,064"))).PN.
8	IS&R	S8	16	((("20020187610") or ("20020153587") or ("20020140039") or ("20020105039") or ("20020093053") or ("20030089930") or ("6,580,137") or ("6,518,127") or ("6,506,638") or ("6,483,156") or ("6,472,258") or ("6,365,465") or ("6,064,589") or ("5,773,331") or ("5,677,550") or ("5,436,506"))).PN.
9	IS&R	S10	3	((("5,349,228") or ("5,273,921") or ("3,755,012"))).PN.
10	IS&R	S11	4	((("4360897") or ("20030149902") or ("20030067321") or ("5612563"))).PN.
11	BRS	S12	1	10/769649
12	BRS	S15	454	(capacitor) and (((silicon adj1 oxynitride) or (SiON)) with thickness with ((silicon adj1 nitride) or SiN))
13	BRS	S16	275	S15 and @ad<="20020619"

	DBs	Time Stamp	Comments	Error Definition
1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/23 14:28		
2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/23 17:03		
3	US-PGPUB; USPAT	2005/05/23 15:47		
4	US-PGPUB; USPAT	2005/05/23 15:47		
5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/24 09:34		
6	US-PGPUB; USPAT	2005/05/23 18:16		
7	US-PGPUB; USPAT	2005/05/23 18:23		
8	US-PGPUB; USPAT	2005/05/23 18:25		
9	US-PGPUB; USPAT	2005/05/23 18:47		
10	US-PGPUB; USPAT	2005/05/23 19:00		
11	US-PGPUB; USPAT	2005/05/24 07:39		
12	US-PGPUB; USPAT	2005/05/24 07:49		
13	US-PGPUB; USPAT	2005/05/24 07:45		

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	Type	Ref #	Hits	Search Text
14	BRS	S17	31	((capacitor) with (((silicon adj1 oxynitride) or (SiON)) with thickness with ((silicon adj1 nitride) or SiN))) and S16
15	BRS	S18	50	((capacitor) with ((silicon adj1 oxynitride) or (SiON)) with thickness) and @ad<="20020619"
16	BRS	S19	19	S18 not S17
17	BRS	S20	406	("or") and (precharge adj1 transistor)
18	BRS	S21	495	("or") with (precharge)
19	IS&R	S22	1	("20020093053").PN.
20	IS&R	S23	2	((("6580137") or ("20020105039"))).PN.

	DBs	Time Stamp	Comments	Error Definition
14	US-PGPUB; USPAT	2005/05/24 08:21		
15	US-PGPUB; USPAT	2005/05/24 08:22		
16	US-PGPUB; USPAT	2005/05/24 08:22		
17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/24 09:37		
18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/24 12:03		
19	US-PGPUB; USPAT	2005/05/24 14:26		
20	US-PGPUB; USPAT	2005/05/24 12:12		

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